

WEST Search History

Hide Items

Restore

Clear

Cancel

DATE: Thursday, April 28, 2005

09/915,833

Hide?	<u>Set</u> <u>Name</u>	<u>Query</u>	<u>Hit</u> <u>Count</u>
	DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR		
<input type="checkbox"/>	L102	197 and L101	9
<input type="checkbox"/>	L101	multiprocessor.ti.	7309
	DB=USPT; PLUR=YES; OP=OR		
<input type="checkbox"/>	L100	5838766.pn.	1
<input type="checkbox"/>	L99	6092098.pn.	1
<input type="checkbox"/>	L98	6233630.pn.	1
	DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR		
<input type="checkbox"/>	L97	184 same FIFO	89
<input type="checkbox"/>	L96	FIFO and L95	16
<input type="checkbox"/>	L95	184 and L94	16
<input type="checkbox"/>	L94	139 and 146	152
<input type="checkbox"/>	L93	L92.ti.	6
<input type="checkbox"/>	L92	message adj FIFO	258
	DB=USPT; PLUR=YES; OP=OR		
<input type="checkbox"/>	L91	4980852.pn.	1
	DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR		
<input type="checkbox"/>	L90	L38 and L89	6
<input type="checkbox"/>	L89	L87 and L26	16
<input type="checkbox"/>	L88	L87 and L37	0
<input type="checkbox"/>	L87	(fifo or queue) same L86	856
<input type="checkbox"/>	L86	L85.ti.	226933
<input type="checkbox"/>	L85	L84 or L39	872942
<input type="checkbox"/>	L84	multiple adj processor\$1	12720
	DB=USPT; PLUR=YES; OP=OR		
<input type="checkbox"/>	L83	6112286.pn.	1
<input type="checkbox"/>	L82	6003123.pn.	1
<input type="checkbox"/>	L81	5944789.pn.	1
<input type="checkbox"/>	L80	5938765.pn.	1
<input type="checkbox"/>	L79	5892915.pn.	1
<input type="checkbox"/>	L78	5737605.pn.	1

INVENTOR NAME
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<input type="checkbox"/>	L77 4691280.pn.	1
<input type="checkbox"/>	L76 3921145.pn.	1
<input type="checkbox"/>	L75 4015243.pn.	1
<input type="checkbox"/>	L74 4041461.pn.	1
<input type="checkbox"/>	L73 4073005.pn.	1
<input type="checkbox"/>	L72 4130864.pn.	1
<input type="checkbox"/>	L71 4144563.pn.	1
<input type="checkbox"/>	L70 4313161.pn.	1
<input type="checkbox"/>	L69 4322795.pn.	1
<input type="checkbox"/>	L68 4045781.pn.	1
<input type="checkbox"/>	L67 4345309.pn.	1
<input type="checkbox"/>	L66 4395753.pn.	1
<input type="checkbox"/>	L65 4161024.pn.	1
<input type="checkbox"/>	L64 4096567.pn.	1
<input type="checkbox"/>	L63 4078254.pn.	1
<input type="checkbox"/>	L62 4075686.pn.	1
<input type="checkbox"/>	L61 4056844.pn.	1
<input type="checkbox"/>	L60 3566358.pn.	1
<input type="checkbox"/>	L59 4410944.pn.	1
<input type="checkbox"/>	L58 4445174.pn.	1
<input type="checkbox"/>	L57 6594735.pn.	1
<input type="checkbox"/>	L56 6604182.pn.	1
	<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>	
<input type="checkbox"/>	L55 L37 and L54	3
<input type="checkbox"/>	L54 L39 and L50	4133
<input type="checkbox"/>	L53 L37 and L51	0
<input type="checkbox"/>	L52 L51.ti.	0
<input type="checkbox"/>	L51 fifo same L50	381
<input type="checkbox"/>	L50 message adj queue\$1	5980
<input type="checkbox"/>	L49 L46.ti.	0
<input type="checkbox"/>	L48 L46 and L39	152
<input type="checkbox"/>	L47 L37 and L46	0
<input type="checkbox"/>	L46 fifo same message adj buffer\$1	170
	<i>DB=USPT; PLUR=YES; OP=OR</i>	
<input type="checkbox"/>	L45 5949982.pn.	1
	<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>	
<input type="checkbox"/>	L44 L38 and L43	19
<input type="checkbox"/>	L43 messag\$3 and L41	19

<input type="checkbox"/>	L42	meessag\$3 and L41	0
<input type="checkbox"/>	L41	L38 and L40	46
<input type="checkbox"/>	L40	L37 and L39	135
<input type="checkbox"/>	L39	multiprocessor\$1 or coprocessor\$1 or processors	872942
<input type="checkbox"/>	L38	shared adj memory	17459
<input type="checkbox"/>	L37	(700/5).ccls.	176
<input type="checkbox"/>	L36	700/5	196
<input type="checkbox"/>	L35	L18 and L34	0
<input type="checkbox"/>	L34	L30 and L33	42
<input type="checkbox"/>	L33	queue\$1 or fifo\$1	104705
<input type="checkbox"/>	L32	L30 and L31	0
<input type="checkbox"/>	L31	message adj circuit\$1	766
<input type="checkbox"/>	L30	L28 and L29	67
<input type="checkbox"/>	L29	(microprocessors or processors) same (shared adj memor\$3)	7555
<input type="checkbox"/>	L28	L26 and L27	87
<input type="checkbox"/>	L27	L23 same L24	624
<input type="checkbox"/>	L26	L5 or L13 or L25	11460
<input type="checkbox"/>	L25	(710/100 710/300 710/301 710/302 710/303 710/304 710/104 710/105 710/106 710/107 710/108 710/109 710/110 710/111 710/112 710/113 710/114 710/115 710/116 710/117 710/118 710/119 710/120 710/121 710/122 710/123 710/124 710/125 710/305 710/306 710/307 710/308 710/309 710/310 710/311 710/312 710/313 710/314 710/315 710/316 710/317).ccls.	9135
<input type="checkbox"/>	L24	shared adj memor\$3	17462
<input type="checkbox"/>	L23	system adj bus\$2	53658
<input type="checkbox"/>	L22	address and L21	2
<input type="checkbox"/>	L21	command\$1 and L20	2
<input type="checkbox"/>	L20	fifo and L19	5
<input type="checkbox"/>	L19	(shared adj memory) and L18	20
<input type="checkbox"/>	L18	(multiprocessor same message\$1).ti.	121
<input type="checkbox"/>	L17	L6 and L16	15
<input type="checkbox"/>	L16	pipeline and L15	18
<input type="checkbox"/>	L15	fifo and L14	68
<input type="checkbox"/>	L14	multiprocessor\$1 and L13	68
<input type="checkbox"/>	L13	L8 and L12	157
<input type="checkbox"/>	L12	(711/118 711/119 711/120 711/121 711/122 711/123 711/124 711/125 711/126 711/127 711/128 711/129 711/130 711/131 711/132 711/133 711/134 711/135 711/136 711/137 711/138 711/139 711/140 711/141 711/142 711/143 711/144 711/145 711/146 711/147 711/148 711/149 711/150 711/151 711/152 711/153 711/154 711/155 711/156 711/157	14770

	711/158 711/159 711/160 711/161 711/162 711/163 711/164 711/165 711/166 711/167 711/168 711/169).ccls.	
<input type="checkbox"/>	L11 L7 and L10	0
<input type="checkbox"/>	L10 circuit same L9	5
<input type="checkbox"/>	L9 pipeline same L8	62
<input type="checkbox"/>	L8 message\$1 same fifo	3256
<input type="checkbox"/>	L7 multiprocessor\$1 same (shared adj memor\$3)	2501
<input type="checkbox"/>	L6 multiprocessor\$1 and (shared adj memor\$3)	4484
<input type="checkbox"/>	L5 L4 or L3 or L2 or L1	2347
	<i>DB=USPT; PLUR=YES; OP=OR</i>	
<input type="checkbox"/>	L4 (712/246 712/247 712/248).ccls.	250
<input type="checkbox"/>	L3 (712/203).ccls.	106
<input type="checkbox"/>	L2 (712/32 712/33 712/34 712/35 712/36 712/37 712/38 712/39 712/40 712/41 712/42 712/43).ccls.	1576
<input type="checkbox"/>	L1 (719/312 719/313 719/314).ccls.	451

END OF SEARCH HISTORY